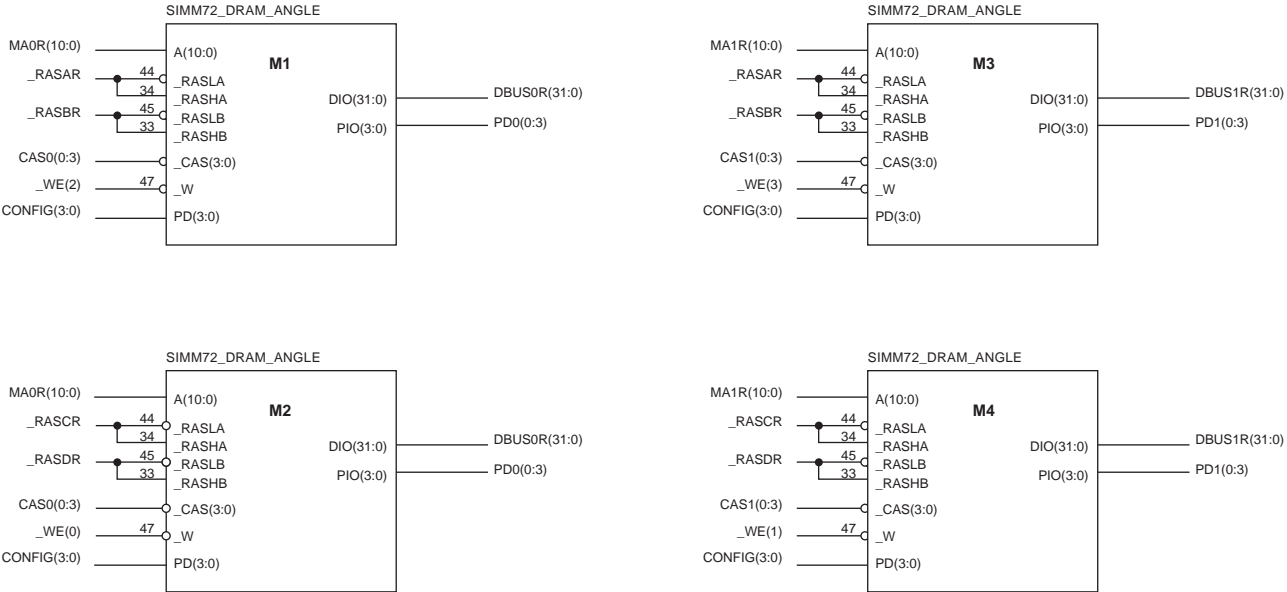
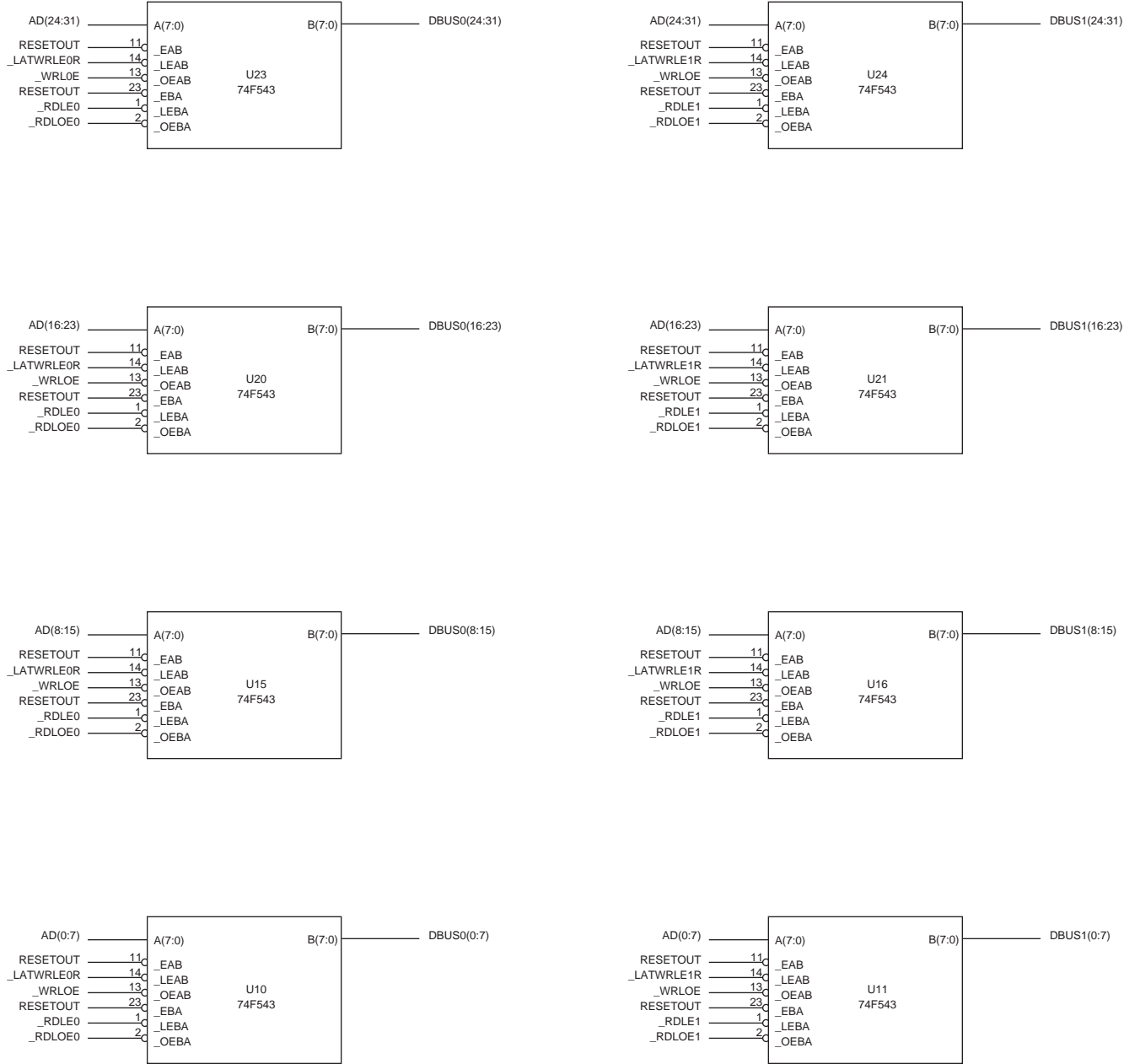
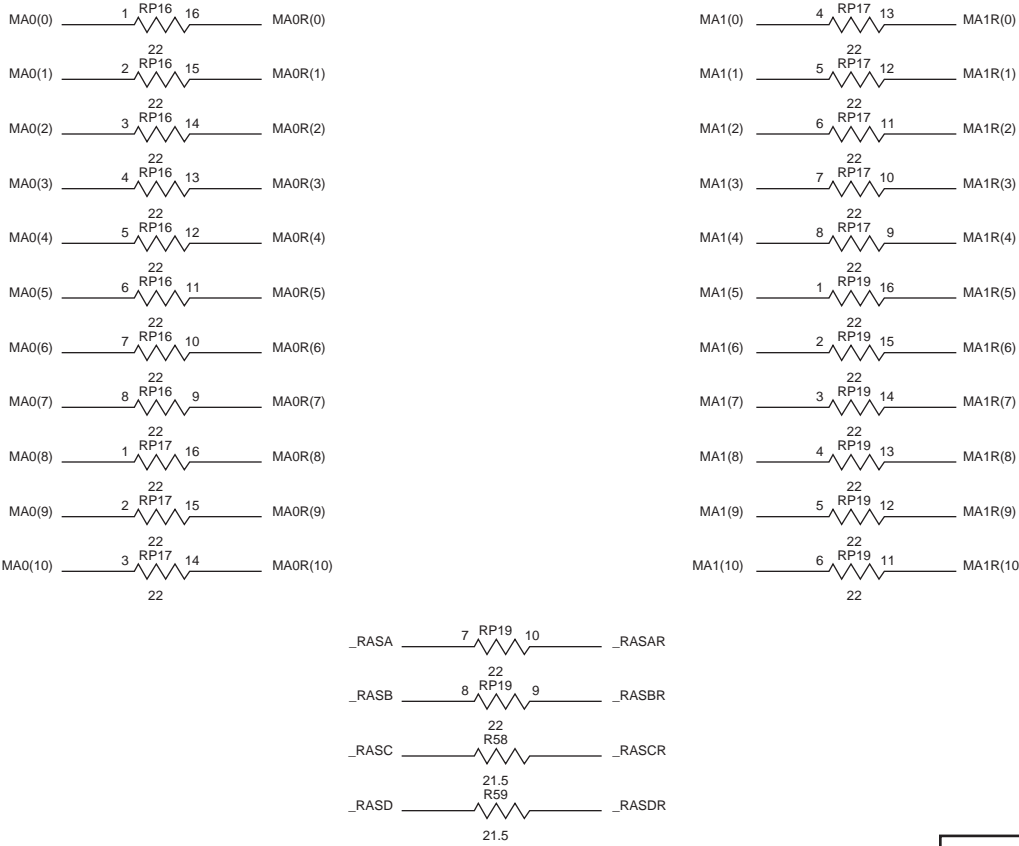


INTERLEAVE LATCHES

DRAM SIMM ARRAY



DRAM ADDRESS IMPEDANCE CONTROL RESISTORS



DRAM : BUS MULTIPLEXER